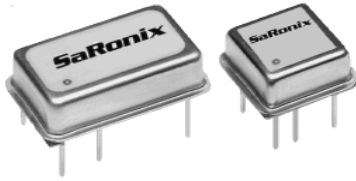


### Technical Data

S1300 / S1309 Series



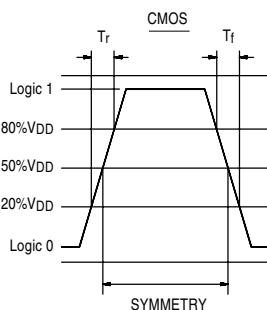
#### Description

A 3.3V, voltage controlled crystal oscillator designed primarily to be used in phase locked loops, phase shift keying, jitter reduction and other telecommunication applications. The HCMOS output can drive both high speed CMOS and TTL loads. Devices are packaged in standard 14-pin DIP compatible all metal, resistance welded packages. Pin 7(4 on 1/2 size) is grounded to reduce EMI.

#### Applications & Features

- 3.3 Volt operation
- HCMOS / TTL compatible
- 3.5ps max RMS period jitter
- Wide range of performance options available:  $\pm 50$  to  $\pm 100$ ppm APR\*;  $\pm 20$  to  $\pm 50$ ppm frequency stability
- Tri-State version available
- Gull Wing for IR reflow available

#### Output Waveform



<b>Frequency Range:</b>	1.5 MHz to 28.6363 MHz
<b>Frequency Stability:</b>	$\pm 20$ , $\pm 25$ or $\pm 50$ ppm over all conditions: operating temperature, voltage change, load change, calibration tolerance, shock and vibration, with $V_C = 1.65V$
<b>Aging @ 25°C:</b>	$\pm 3$ ppm max per year, $\pm 10$ ppm max for 10 years
<b>Temperature Range:</b>	Operating: 0 to +70°C or -40 to +85°C Storage: -55 to +125°C
<b>Supply Voltage:</b>	Recommended Operating: 3.3V $\pm 10\%$
<b>Supply Current:</b>	10mA typ, 15mA max
<b>Output Drive:</b>	Symmetry: 45/55% max @ 50% VDD Rise & Fall Times: 9ns max 20% to 80% VDD Logic 0: 10% VDD max Logic 1: 90% VDD min Load: 30pF Jitter: 3.5ps max RMS period jitter

#### Pull Characteristics:

Input Impedance (pin 1):	50K $\Omega$ min
Frequency Response (-3dB):	10 kHz min
Pullability:	$\pm 50$ , $\pm 70$ , $\pm 100$ ppm APR* min
Control Voltage:	0.3 to 3.0V
Transfer Function:	Frequency Increases when Control Voltage Increases
Linearity:	5 or 10% max
Center Control Voltage:	1.65V

#### Mechanical:

Shock:	MIL-STD-883, Method 2002, Condition B
Solderability:	MIL-STD-883, Method 2003
Terminal Strength:	MIL-STD-202, Method 211, Conditions A & C
Vibration:	MIL-STD-883, Method 2007, Condition A
Solvent Resistance:	MIL-STD-202, Method 215
Resistance to Soldering Heat:	MIL-STD-202, Method 210, Conditions A, B or C (I or J for Gull Wing)

#### Environmental:

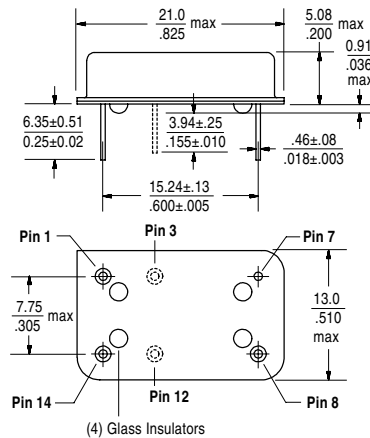
Gross Leak Test:	MIL-STD-883C, Method 1014, Condition C
Fine Leak Test:	MIL-STD-883C, Method 1014, Condition A2
Thermal Shock:	MIL-STD-883C, Method 1011, Condition A
Moisture Resistance:	MIL-STD-883C, Method 1004

\* APR = (VCXO Pull relative to specified Output Frequency) – (VCXO Frequency Stability)

### Technical Data

### S1300 / S1309 Series

#### Package Details

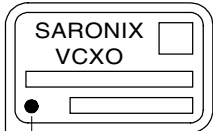


#### Pin Function:

- Pin 1: Control Voltage
- Pin 3: Tri-State control (optional)
- Pin 7: GND/Case (VSS)
- Pin 8: OUTPUT
- Pin 12: N/C (optional)
- Pin 14: +3.3VDC (VDD)

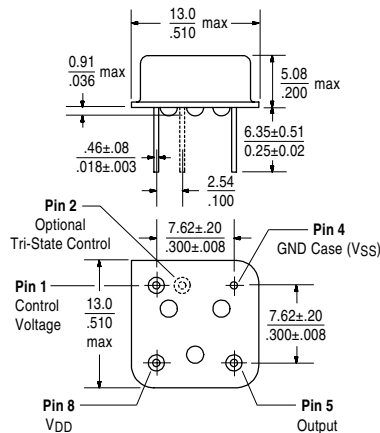
#### Marking Format\*\*

Includes Date Code, Frequency & Model



Denotes Pin 1

#### HALF SIZE PACKAGE



#### Marking Format\*\*

Includes Date Code, Frequency & Model

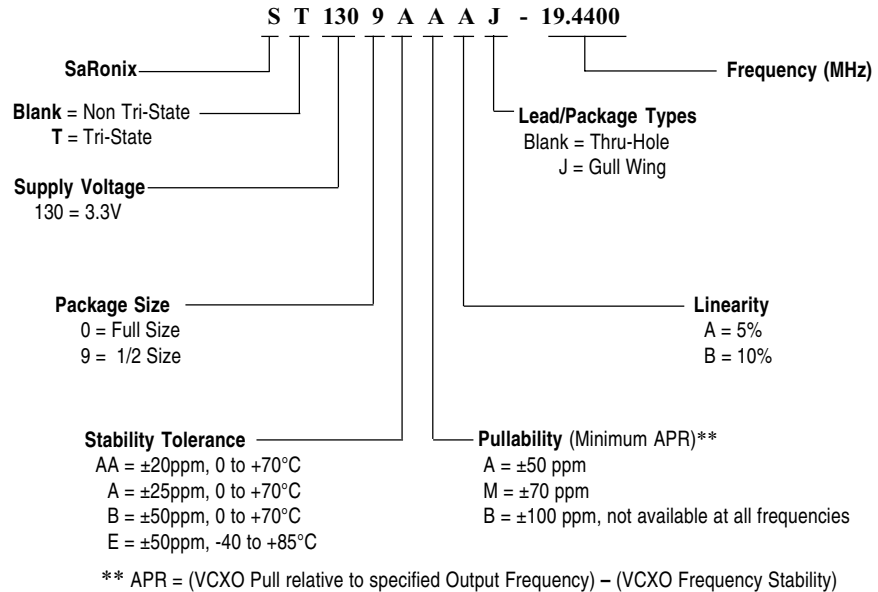


Denotes Pin 1

\*\*Exact location of items may vary

Scale: None (Dimensions in  $\frac{\text{mm}}{\text{inches}}$ )

#### Part Numbering Guide



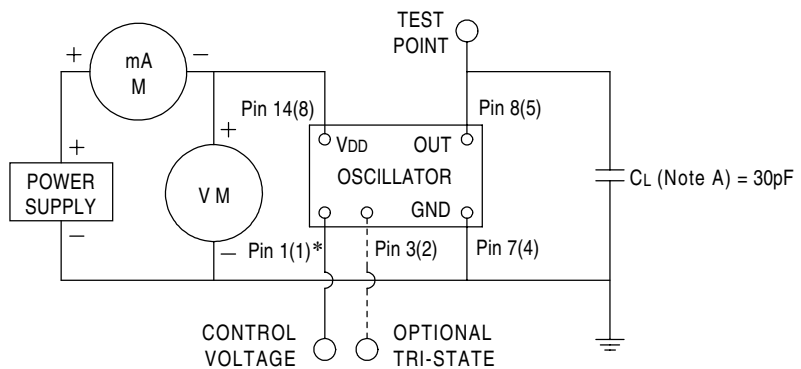
#### Tri-State Logic Table

Pin 3(2) Input	Pin 8(5) Output
Logic 1 or NC	Oscillation
Logic 0 or GND	High Impedance

Required Input Levels on Pin 3(2):

- Logic 1 = 70% V<sub>DD</sub> min
- Logic 0 = 30% V<sub>DD</sub> max

#### Test Circuit



NOTE A: C<sub>L</sub> includes probe and fixture capacitance

\* Items in brackets ( ) represent Half Size model

All specifications are subject to change without notice.